

VLSI DESIGN WEBINAR REPORT



Facilitator: BABU M, AP/ECE, RMKCET

In the overview of the COVID-19 situation in our state and the country, we have had to take stringent lockdown measures for the safety of our family and society. But education and learning needs to be continued even in dire situations like the one we are enduring of late. To prepare us for the upcoming semester examination as well as to boost our foundation regarding VLSI, Mr. Babu M, AP/ECE had organized zoom classes for the third-year students. We had about 10 sessions covering intricately and in detail an array of important concepts as listed below.

- 11.04.2020 MOS Transistor Theory
- 12.04.2020 DC Transfer Characteristics of CMOS
- 13.04.2020 CMOS Design
- 14.04.2020 Stick Diagram and Layout Design
- 15.04.2020 Delay Estimation and Scaling Principles
- 16.04.2020 Combinational Circuit: Static CMOS Design
- 17.04.2020 Combinational Circuits: Dynamic CMOS Design
- 20.04.2020 Design of Static Latches and Registers
- 21.04.2020 Design of Dynamic Latches and Registers
- 23.04.2020 Introduction to Verilog HDL

The contents of our sessions were extremely useful despite internet/bandwidth difficulties for a few students. Mr. Babu M, AP/ECE was kind enough to share the

recorded sessions through Google Drive and YouTube (<https://youtube.com/aksharam>) which benefitted those who couldn't attend the live sessions.

Our doubts were clarified and we definitely could use more of his classes via Zoom a few weeks before our commencement of the postponed Anna University Examination. Even if our exams cancelled, if we were to have placements for core companies, VLSI Zoom classes conducted by Babu M, AP/ECE will be useful.

